Politecnico di Torino
&
CINI

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Who we are

- **One of the leading European research and development team in Digital System Testing and Dependability**

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Our Mission

***R&D*-ing on Digital System Design, Testing, Dependability, and Accessibility to properly serve companies, R&D centers, and students.**

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Our consolidated know-how

- **Core, Component, Board, & System Testing**
- **Microprocessor & Memory Testing**
- **Design-for Testability, Built-In Self Test, Built-in Self Repair**
- **System level modeling**
- **Design Validation & Verification**
- **Dependability Analysis & Evaluation**
- **Fault Injection Techniques**
- **Accessibility - Deaf People**

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Our interests here...

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Future of Internet

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ATLAS : Automatic Translation into Sign Languages

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Goal

- **Exploiting the convergence between cognitive sciences and ICT to build innovative services to provide *deaf* people with the access to broadcast communications, through the automatic translation of the written natural language into the animated Sign Languages.**

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Challenge 3

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Flash Memories Design for Space Applications

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ExoMars Mission

Aim:

- **further characterize the biological environment on Mars**
- **preparation for robotic and human exploration**
- **gather data for Exobiology (*the search for life on other planets*)**

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Advanced Microprocessors On-line Testing

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Goal

- **Developing a user-oriented on-line testing of advanced microprocessors (PowerPC, Pentium) for high dependable applications (High Speed Trains control)**

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Memory Self-Testing, Self-Diagnosing & Self-Repairing

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System Level Design & Modeling via TLM

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- **Consortium of 30+ Italian Universities (Computer Engineering & Computer Science)**

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Малые Автюхи, Калинковичский район, Республики Беларусь